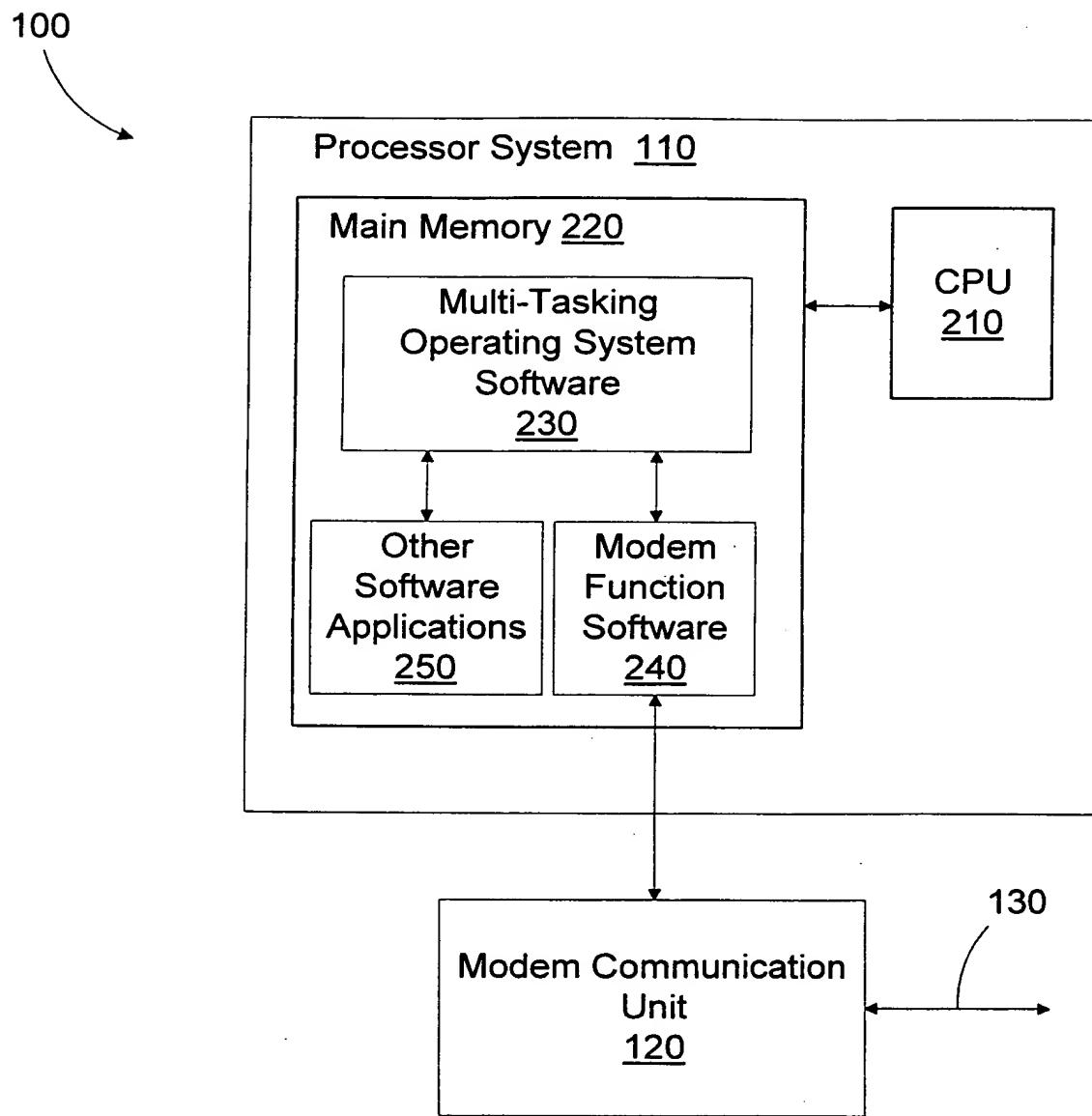
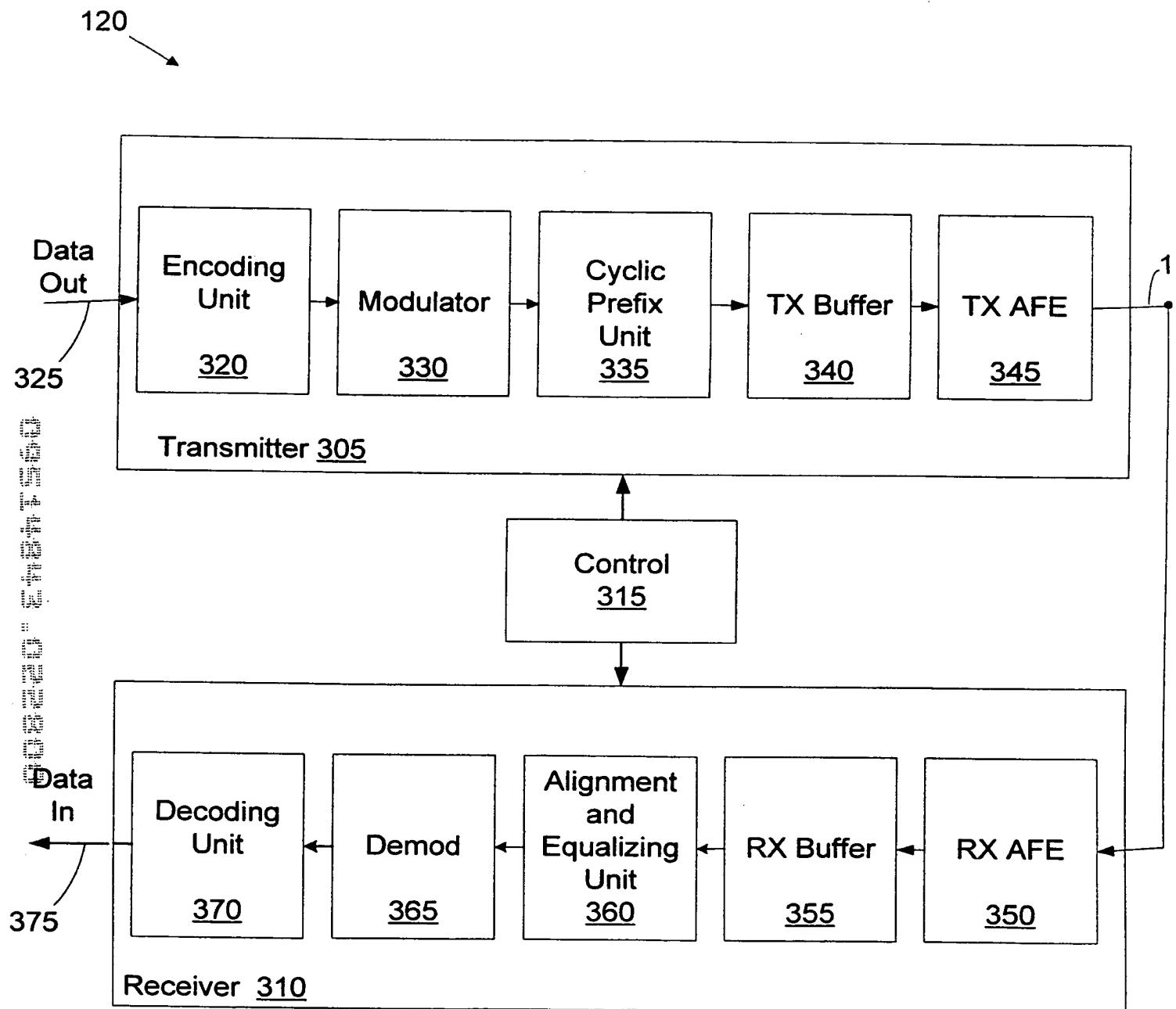


**Figure 1**



**Figure 2**



**Figure 3**

Modulated  
Symbol  
Data

**TX Buffer 340**

Buffer Logic  
415  
Counter  
420

420

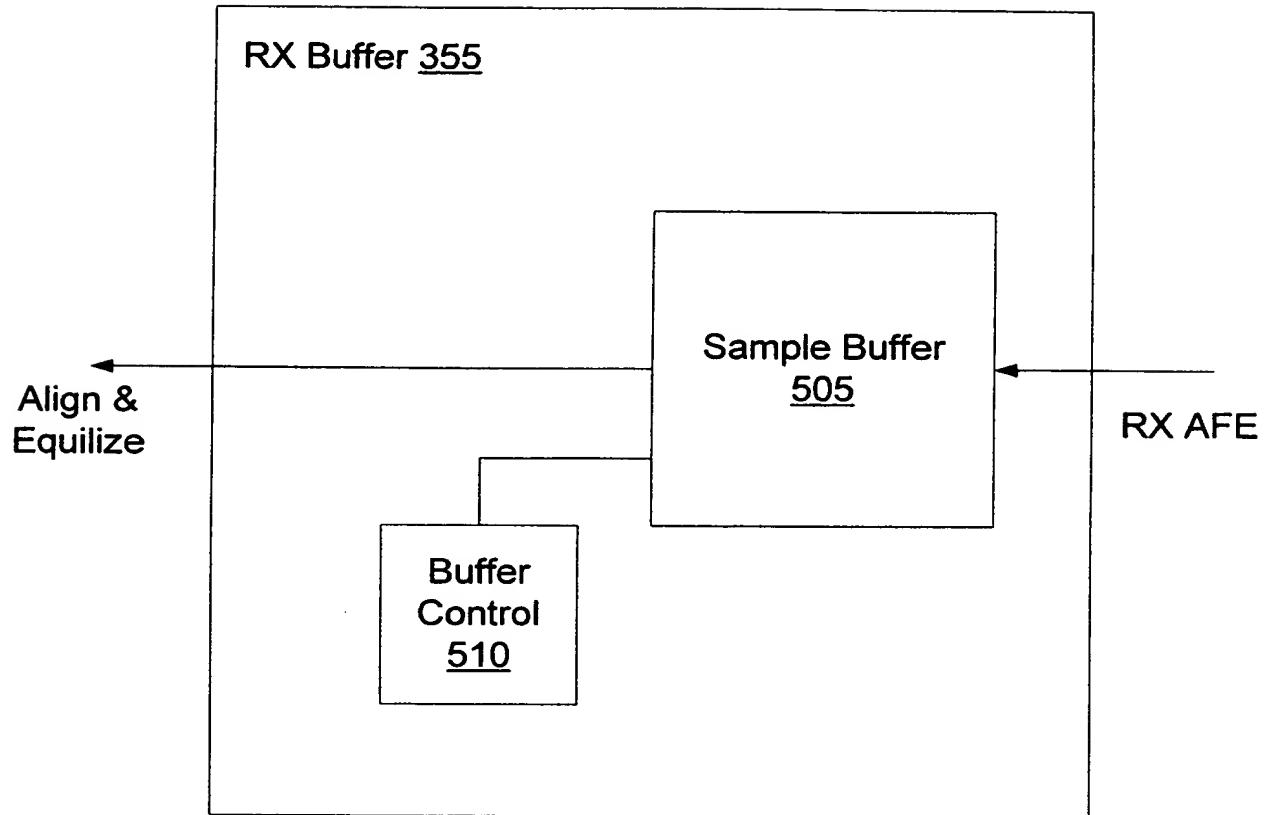
Idle  
Symbol  
Buffer  
410

425

Sample Buffer  
405

TX AFE

**Figure 4**



**Figure 5**